

EMOTIONALLY INTELLIGENT ALGORITHMS
DEALING SOME PRACTICAL COMBINATORIAL PROBLEMS

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ABSTRACT

As per the research in the field of Neuro Sciences, our Subconscious mind actually controls our emergency motor neurons for smooth functioning our most vital mussels such as Heart, Lungs, Kidneys, Brain and other internal and external mussels to live us bypassing conscious logical decisions making memory, the conscious memory, and that is why our subconscious memory functions the most important role to live us and to deal with internal and external problems. This subconscious mind controls our emotions. That is why the emotionally intelligent human can take better decisions than a machine.

The aim of this paper is to make man machine interaction more efficient by using emotionally intelligent combinatorial algorithms.

Key Words: Emotional Intelligence (EI), Emotionally Intelligent Algorithm (EIA)

Definition 1: Emotional Intelligence (EI)

From medical Science point of view-The capacity to be aware of, control and express one's emotions and to handle interpersonal relationships judiciously and empathically.

Definition 2: Emotionally Intelligent Algorithm (EIA):

Literally in practical daily life some time we do some work by emotion, apparently it is for something different than the desired problem's solution, but the side effect of the work actually solves the target problem indirectly. Thus by not attacking the problem directly, but dealing it diplomatically to solve it efficiently, without loosing so much energy, we call these algorithms as Emotionally Intelligent Algorithms.

PROCEDURE: GRAY_CODE_MAT

Step1: GM(i) ← i $\forall i=1, 2, 3, \dots$
Step2: Copy Entire GM to TGM
Step3: i ← 0, r ← 0
Step4: If TGM(i) ≠ 0 Then Continue Else Goto Step 14
Step5: n = PAT(r) = TGM(i)
Step6: r ← r + 1
Step7: Shift Left one bit of TGM
Step8: j = Position of n in GM
Step9: IF j ≠ 0 Then Continue Else Goto Step 13
Step10: Shift Right TGM by one bit and Assign the value in GM
Step11: End IF
Step12: End If
Step13: Repeat through Step 4
Step14: Return

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EXPLANATION OF THIS ALGORITHM

This Algorithm generates **All possible gray codes sequentially up to n bits (Problem 1)**.

While generating these bit-sequences the intermediate array **PAT []** is nothing but the Palindrome number, that is this algorithm generates **Palindrome Number Sequences (Problem 2)**.

Using this Algorithm after nominal modification intelligently it can generate the **Power Set $P(S)$ of a given set S (Problem 3)**.

This also leads to the solution of the problem of generation of **All Possible Combination of a set of numbers (Problem 4)** with minimal addition alteration over the algorithm intelligently.

Hence we call this Algorithm as Emotionally Intelligent Algorithm.

COMPUTATIONAL COMPLEXITY:

Let, number of vertices for the given graph G is V , number of edges is E and the number of tree possible for the graph is N . Then the algorithm must output N number of trees. Now, to generate a single tree form the module **ALL_TREES**:

(i) To generate T_0 the required time complexity is of **$O(E \log(V) + V)$** .

(ii) For the module **Gray_Code_Mat** **$O(V^2)$**

(iii) For the module FCM time required is **$O(V^2)$**

Since, in the limiting case N actually dominating factor over V^2 then over all time complexity for the proposed algorithm is **$O(E \log(V) + V + N)$** .

Now the space complexity for the algorithm is only the space required to represent the single tree. And since, only single level trees are required to store then at most V rows are required. Hence the space required for this algorithm is **$O(V^2)$** .

Since, N represents number of trees it may be exponential in the worst case. Obviously, the conclusion is trivial as we are generating all trees of any graph.

REFERENCE

1. Rao B. and V. Murti. G. K. Enumeration of All Trees a Graph Computer Program, Electronics Letters, Vol. 6, No. 4, 1970.
2. B. Rao and V. G. K. Murti. Enumeration of All Trees a Graph Computer Program, Electronics Letters, Vol. 6, No. 4, 1970.
3. Trent H. M., A Note on the Enumeration and Listing of All Possible Trees in a Connected Linier Graph, Proc. Nat. Acad. Sci. U.S.A., Vol. 40, p. 1004.
4. Pak I. and Postnikov. A., Enumeration of Spanning Trees of Graphs, Harvard University, Massachusetts Institute of Technology, 1994.
5. Peikarski M., Listing of All Possible Trees of a Linear Graph, Ibid., CT-12, Correspondence, pp. 124-125, 1965.
6. DeoN. , Graph Theory with Applications to Engineering and Computer Science. Prentice-Hall of India Private Limited, New Delhi, 2003.
7. Reingold, Nievergelt and Deo, Combinatorial Algorithms, Prentice-Hall, Inc., 1977.
8. Hakimi S.L, On the Trees of a Graph and Their Generation. J. Franklin Inst. 270, pp. 347-359, 1961.
9. Seshu S. and Reed M. B, Linear Graphs and Electrical Networks, Rading, Mass, Addison-Wesley, 1961.
10. Sen Sarma S., . Rakshit A, Sen R. K. , Choudhury A. K., An Efficient Tree Generation Algorithm, Journal of the Institution of Electronics and Telecommunications Engineers (IETE), Vol. 27, No. 3, pp. 105-109, 1981.
11. Mayeda W., Graph Theory, Wiley Inter-science, 1972.
12. Mayeda W. & Seshu S., Generation of Trees without Duplications, IEEE Trans, CT-12, pp. 181-185, 1965.
13. Arumugam S. and Ramachandran S., *Invitation to Graph Theory*, 1st ed., Scitech Publication (India) Pvt. Ltd., Chennai, 2002.
14. DeoN., *Graph Theory with Applications to engineering and Computer Science*, Prentice Hall of India Pvt. Ltd., New Delhi, 2005.
15. Horowitz E., Sahani S., Rajasekaran S., *Fundamentals of Computer Algorithms*, 2nd ed., Universities Press (India) Pvt. Ltd (2008).
16. The Mathworld website.[Online]. Available: <http://mathworld.wolfram.com/>
17. Konneth Sorensen, Gerrit K. Janssens, "An Algorithm to Generate all Spanning Trees of a Graph in Order of Increasing Cost", University of Antwerp; Hasselt University Belgium(2005).
18. A First Look at the Graph Theory:-Clark, Holton.

19. Garey M. R. and Johnson D.S, Computers and Intractability: A Guide to the Theory of NP-Completeness, Freeman, San Francisco, 1979.
20. Chartrand G. and Lesniak L., Graphs & Digraphs, Third Edition, Chapman & Hall, 1996.
21. Hartsfield N. and Ringle G., Pearls in Graph Theory A Comprehensive Introduction, Academic Press, INC, Harcourt Brace Jovanovich, publishers, 1997.
22. E. Charbon and I. H. Torunoglu, "On intellectual property protection," in Proceedings of the IEEE Custom Integrated Circuits Conference, Orlando, USA, 2000, pp. 517–522.
23. A. B. Kahng, J. Lach, W. H. Mangione-Smith, S. Mantik, I. L. Markov, M. Potkonjak, P. Tucker, H. Wang, and G. Wolfe, "Constraint-based watermarking techniques for design ip protection," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 20, no. 10, pp. 1236–1251, 2001.
24. W. Stallings, Cryptography and network security. Prentice-Hall India, 2005.
25. Saha D., Dasgupta P., Sur-Kolay S., and Sen-Sarma S., "A novel scheme for encoding and watermark embedding in vlsi physical design for ip protection," in Proceedings of the International Computing Conference: Theory and Algorithms, ICCTA '07. Kolkata, India: IEEE Computer Society, March 2007, pp. 111–116.
26. Saha D. and Sur-Kolay S., "Encoding of floorplans through deterministic perturbation," in Proceedings of the 22nd International Conference on VLSI Design, VLSID '09. New Delhi, India: IEEE Computer Society, January 2009, pp. 315–320.
27. A. T. Abdel-Hamid, S. Tahar, and E. M. Aboulhamid, "A public-key watermarking technique for ip designs," in Proceedings of the conference on Design, Automation and Test in Europe, DATE '05. Messe Munich, Germany: IEEE Computer Society, March 2005, pp. 330–335.
28. G. Qu, "Publicly detectable watermarking for intellectual property authentication in vlsi design," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 21, no. 11, pp. 1363– 1368, 2002.
29. E. Castillo, L. Parrilla, A. Garcia, A. Loris, and U. Meyer-Baese, "Ipp watermarking technique for ip core protection on fpl devices," in Proceedings of the International Conference on Field Programmable Logic and Applications. Madrid, Spain: IEEE Press, August 2006, pp. 1–6.
30. Y. C. Fan and H. W. Tsao, "Watermarking for intellectual property protection," IEEE Electronics Letters, vol. 39, no. 18, pp. 1316–1318, 2003.
31. P.-C. Chen, Y.-S. Chen, and W.-H. Hsu, "A communication system model for digital image watermarking problems," IAENG International Journal of Computer Science, vol. 34, no. 2, November 2007.
32. J. P. Hayes, Computer Architecture and Organization. Tata Mc-graw Hill, 1996.
33. M. Abramovici, M. A. Breuer, and A. D. Friedman, Digital System testing and Testable Design. Jaico Publishers, 2001.
34. Halder R., Dasgupta P.S., Naskar S., and S. Sarma S., "An internet-based ip protection scheme for circuit designs using linear feedback shift register (lfsr)-based locking," in Proceedings of the 22nd Annual Symposium on Integrated Circuits and System Design, SBCCI '09. Natal, Brazil: ACM Press, 31st Aug–3rd Sep 2009.
35. D. Kirovski and M. Potkonjak, "Localized watermarking: methodology and application to template mapping," in Proceedings of the IEEE International Conference on the Acoustics, Speech, and Signal Processing, ICASSP '00. Istanbul, Turkey: IEEE Computer Society, June 2000, pp. 3235–3238.
36. E. Charbon, "Hierarchical watermarking in ic design," in Proceedings of the IEEE Custom Integrated Circuits Conference. Santa Clara, CA, USA: IEEE Press, May 1998, pp. 295–298.
37. E. Charbon and I. Torunoglu, "Intellectual property protection via hierarchical watermarking," in Proceedings of the International Workshop on IP Based Synthesis and System Design, Grenoble, France, Dec 1998, pp. 776–781.
38. J. Lach, W. Mangione-Smith, and M. Potkonjak, "Fpga fingerprinting techniques for protecting intellectual property," in Proceedings of the IEEE Custom Integrated Circuits Conference. Santa Clara, CA, USA: IEEE Press, May 1998, pp. 299–302.
39. A. L. Oliveira, "Robust techniques for watermarking sequential circuit designs," in Proceedings of the 36th annual ACM/IEEE Design Automation Conference, DAC '99. New Orleans, Louisiana, USA: ACM Press, June 1999, pp. 837–842.
40. I. Torunoglu and E. Charbon, "Watermarking-based copyright protection of sequential functions," IEEE Journal of Solid-State Circuits, vol. 35, no. 3, pp. 434–440, 2000.
41. R. Merkle, Secrecy, authentication, and public key systems. Stanford Ph.D. Thesis, 1979.
42. M. Robshaw, "Stream ciphers," RSA Laboratories Technical Report TR-701, Version 2.0, Tech. Rep., 1995.
43. S. V. Sathyanarayana, M. K. Aswatha, and K. N. H. Bhat, "Symmetric key image encryption scheme with key sequences derived from random sequence of cyclic elliptic curve points," International Journal of Network Security, vol. 12, no. 3, pp. 137–150, 2011.
44. C. J. Alpert, "The ispd98 circuit benchmark suite," in Proceedings of the 1998 international symposium on Physical design, (<http://vlsicad.ucsd.edu/UCLAWeb/cheese/ispd98.html>). Monterey, California, USA: ACM Press, April 1998, pp. 80–85.

45. A. Seth, S. Bandyopadhyay, and U. Maulik, "Probabilistic analysis of cellular automata rules and its application in pseudo random pattern generation," IAENG International Journal of Applied Mathematics, vol. 38, no. 4, November 2008.
46. R. W. Hamming, "Coding and Information Theory", 2nd Edition, Prentice Hall, U.S.A, 1986.
47. R. P. Feynman, "Lectures on Computation", Perseus Books Groups, U.S.A, 1996.
48. G. A. Jones and J. M. Jones, "Information and Coding Theory", Springer, Great Britain, 2000.
49. Reingold E., Nievergelt J., Deo N., "Combinatorial Algorithms: Theory and Practice", Prentice Hall, U.S.A, 1977.
50. D. E. Knuth, "The Art of Computer Programming Volume 2: Seminumerical Algorithms", 2nd Edition, Addison – Wesley, U.S.A, 1981.
51. B. A. Forouzan, "Cryptography and Network Security", Tata McGraw Hill, India, 2007.
52. Banerjee.S., Sensarma, D., Basuli K., Naskar, S. & Sarma, S. S. (2011) "The Reconstruction Conjecture", The Second International Conference on Computer Science and Information Technology, Vol. 86, Part III, pp 17-25.
53. Carrillo, H. & Lipman, D. (1988) "The multiple sequence alignment problem in biology", SIAM Journal of Applied Mathematics, Vol. 48, No.5, pp 1073 – 1082.
54. Clark, J. & Holton, D. A. (1995) "A First Look At Graph Theory". World Scientific Publishing company.
55. Cormen, T. H., Leiserson, C. E. & Rivest, R. L., Stein, C. (2010) "Introduction to Algorithms". 3rd ed. Phi Learning.
56. Farrell, E.J. & Wahid, S.A. (1987) "On the reconstruction of the matching polynomial and the reconstruction conjecture". Int. J. Math. Math. Sci. Vol. 10, pp 155–162.
57. Farrell, E. J. (1979) "Introduction to Matching Polynomials", Journal of Combinatorial Theory, Vol. 27, pp 75-86.
58. Gutman, I. & Cvetkovic, D.M. (1975) "The Reconstruction Problem for Characteristic Polynomials of Graphs", Univ. Beograd. Elektrotehn. Fak. Ser. Mat. Fiz, pp 45-48.
59. Harary, F.(1964) "On the reconstruction of a graph from a collection of subgraphs", Theory of Graphs and its applications (M. Fiedler,ed) Prague, pp 47–52.
60. Harary, F.(1974) "A Survey of the Reconstruction Conjecture in Graphs and Combinatorics", Lecture Notes in Mathematics, Vol. 406, pp 18-28.
61. Hemaspaandra, E., Hemaspaandrab, L. A., Radziszowska & S. P., Tripathib, R. (2004) "Complexity results in graph reconstruction", Discrete Appl math, Vol. 155, pp 103-118.
62. Jerrum, Mark. (1987) "Two-dimensional monomer-Dimer systems are computationally intractable", Journal of Statistical Physics, Vol.48, No.1, pp 121–134.
63. Kelly, P. J.(1942) "On isometric transformations", Ph.D. thesis, University of Wisconsin, USA.
64. Manvel, B. (1988) "Reconstruction of graphs – Progress and prospects. Congressus Numer, [2] B. Bolobas, Modern Graph Theory - Kirchhoff's Matrix Tree Theorem, p. 54, Springer International Edition, New York, 2002.
65. S. Naskar, K. Basuli, R. K. Pal, and S. Sen Sarma, Generation of All Spanning Trees of a Simple, Symmetric, Connected Graph, National Seminar on Optimization Technique, Organized by the Dept. of Applied Mathematics, University of Calcutta, p. 57, 2007.

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